
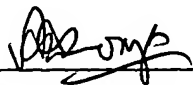


FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket No. 1406/153		Serial No. 10/615,100	
List of Documents Cited by Applicant				Applicant(s): Mario Traeber			
				Filing Date: July 8, 2003		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing date if Appropriate
AD	1.	6,209,109	03/27/2001	Hori et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Name of Patentee or Applicant	Translation Yes   No	
AD	2.	197 42 958 A1	04/29/1999	Germany	Siemens AG	No	
AD	3.	2000 059 238	02/25/2000	Japan	Mitsubishi Electric Corp.	Yes (Abstract Only)	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
AD	4.	Shafiee, H. et al., "A Reduced-Complexity Trellis Search Decoding Algorithm for Extended Class IV Partial Response Systems," <u>IEEE International Conference on Communications</u> , Vol. 4, pp. 120-124 (June 14, 1992).					
AD	5.	Shieh, Ming-Der et al., "Efficient Management of In-Place Path Metric Update And Its Implementation for Viterbi Decoders," <u>IEEE International Symposium on Circuits and Systems</u> , Vol. 4, pp. 449-452 (May 31, 1998).					
AD	6.	Shung, C. Bernard et al., "VLSI Architectures for Metric Normalization in the Viterbi Algorithm," <u>IEEE International Conference on Communications</u> , Vol. 4, pp. 1723-1728 (April 15, 1990).					
AD	7.	Tsui, Chi-ying et al., "Low Power ACS Unit Design for the Viterbi Decoder," <u>IEEE International Symposium on Circuits and Systems</u> , pp. 137-140 (May 30, 1999).					

EXAMINER



DATE CONSIDERED

9/3/2006

\*Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.